

## UNIVERSITY OF THE PUNJAB

M.Sc. I.T. (First Year) Annual Examination - 2022

Roll No. .....

Paper: I-NI / I-NII (Discrete Mathematics + Logic Design & Computer Organization)

Time: 3 Hrs. Marks: 100

## USE SEPARATE ANSWER SHEET FOR EACH PART

NOTE: Question No. 1 is Compulsory. Attempt any TWO questions from remaining Questions.

		PART -	I (DISC	RETE MATHEMAT	rics)				
Qu	estion # 1: Sele	ct the right	answer cı	utting and overwritin	g is not allowed. (10x1=10)				
1.	"Some students wants to learn Physics and some want to learn English." Which of the following quantified expression best represent the statement?								
	a) ∃n∀x b) ∀x∃n			c) 3n V 3x	d) ∃n ∧ ∃n				
2.	Traveling salesm	an problem is	example for	r?					
	a) Planar	b) Undirected		c) Euler	d) Hamiltonian				
3.	The shaded area of figure is best described by:								
		a) A - B - C c) A U B		b)-G — A — B d) B ∩ A					
4.	If $[x] = [x]$ , then which of this following statement is true about x:								
	a) Such value of x does not exist c) x is integer			b) x is zero d) None of the mentioned					
5.	$f(n) = -20n^5 + 987n^3 - 1000 is:$								
	a) O(n3)	b) O(n5)	c) $O(n^6)$	d) Both a and b	e) Both b and c f) None				
6.	The total number of edges in complete graph K5 will be?								
	a) 5	b) 8	c) 10	d) 12					
7.	The relation R defined by $R = \{(1,1),(1,2),(1,3),(2,1),(3,3)\}$ is reflexive.								
	a) True	b) False							
8.	The number of vertices of odd degree in an undirected graph is always								
9.	If we have to ensure that at least four of the students have similar grade in a course (where total possible grades are 9), minimum number of students should be								
10	. If a compound I combinations in	Proposition state the truth table	tement is m	ade up of eight proposit	ions, then the number of distinc				

a) Assume P(x,y) is (2x < (y+2)). Write both  $\exists y \forall x P(x,y), \forall x \exists y P(x,y)$  in expanded form and determine their truth value, where  $[x,y \in \text{set of first four natural numbers}]$ 

Evaluate following summations using n=5, m = 3, a= 2 and b= -2

(6)

 $\sum_{i=1}^{m}\sum_{j=1}^{m}i+i^2+5bj-4a$ 

(4)

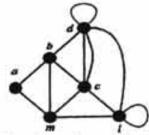
Evaluate following summations by using formulas:

 $\sum_{k=50}^{100} k^3$ ? where  $\sum_{k=1}^{n} k^3 = \left(\frac{n(n+1)}{2}\right)^2$ 

(20 Marks)

Ouestion#3:

a) Represent the following two graphs using Adjacency and Incidence matrix. Also determine that whether the given undirected graph has Hamiltonian circuit/Hamiltonian path, Euler circuit/Euler path. Also determine the chromatic number for the undirected graph. (14)



b) Prove the following using mathematical induction.

(6)

$$1^2 + 2^2 + 3^2 + 4^2 + 5^2 + \dots + n^2 = \frac{n(n+1)(2n+1)}{6}$$

Question#4:

(5+10+5=20)

a) What is the next term in the sequence: {5,23,113,563,2813,14063, ....}? Express the given sequence using recurrence relations. Also clearly write initial value(s).

b) For A = {1,2,3} B={2,4,6} and universal set being first ten natural numbers. Express Set A and B in form of bitstrings. Also compute AUB, A-B, A and express the results using set-representation, bitstrings and vein diagrams.

c) In a programming language: variable names up to four-length are accepted. These variable names can start with a lowercase or uppercase English letter. However, for the remaining three letters, digits and seven-special characters are also allowed. Out of the total possible combinations: eight (8) single-length, sixteen(16) two-length, and forty (40) three-length variable names are reserved words. How many different variable names can be made in this language, excluding the reserved words?

- Convert (163.4)s to decimal number system.
- ii) Let A = 111010 and B = 101010, perform A-B using 1's complement.
- b) Reduce the expression (x'y' + z)' + z + xy + wz to three literals using identities and theorems of Boolean Algebra.
- c) Draw the logic circuit diagram and truth table F(A,B,C) =A'B + B'C.
- d) Draw the circuit diagram of a 2-bit magnitude comparator that compares A1Ae and B1Be. The circuit has only one output E, such that E=1, if the inputs are identical.

(20 Marks) Question#7:

- a) Draw the logic circuit diagram and truth table of 2x4 decoder with an enable input. Also show the construction of 3x8 decoder using two 2x4 decoders.
- b) Implement F(A,B,C)= ∑ (1,4,5,7) using 4x1 multiplexer with variable A,B as a selection line. (6)
- Obtain the simplified expression in POS form for the following function using K-Map. (6) F(A,B,C,D) = (A+B'+D).(A'+C+D').(C+D)

(20 Marks) Question#8:

- Explain instruction cycle and the CPU registers used in an instruction cycle. (10)
- b) Design a 3-bit synchronous counter using T flip flops with the following counting sequence. (10)0 -> 1 -> 3 -> 5 -> 2 -> 4 -> Repeat

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## PART - II (LOGIC DESIGN & COMPUTER ORGANIZATION)

NOTE: Question No. 5 is Compulsory. Attempt any TWO questions from remaining Questions.

Question # 5:		Select the right answer cutting and overwriting is not allowed. (10x1=10)							
1	. Which of the	following code	s is not a weig	ghted code	?				
	A) 2421	B) 84-2-1	C	c) gray	D) Excess	-3			
2,	The smallest n-bit number written in signed 2's complement notation is:								
	A) -2 <sup>n</sup>	) -2 <sup>n</sup> B) -2 <sup>n-1</sup>		C) -2 <sup>n</sup> -1		D) -2 <sup>n-1</sup> -1			
3.	The complement of a Boolean function F(A,B,C) = AB' + A'C?								
	A) (A+B').(A'+C) B) A'+B'A+C			C) (A'+B).(A+C') D) A'B+AC'					
4.	For an expression XY' + XZ + YZ, the consensus term is								
	A) XY B) XZ		C) YZ	D) XY'Z					
5.	Which of the following is a standard SOP representation of F(A,B,C)?								
	A) ABC + AB'C C) A B) A + B + C D) A			TV 1.0 T-17 1774 V					
	A Boolean function F (A,B) = A⊕B can be written as								
	A) $F = A'B + AB'$ B) F		= Π (0,3) C) F =		$=\sum (1,2)$	D) All of the given			
7.	Which of the following CPU register maintains the address of the next instruction to be executed?								
	A) Memory Adress Register     B) Program Counter		0.00	C) Next Address Register D) Next Instruction Register		ter .			
8.	Which one of the following is not a combinational circuit?								
	A) Binary Add	ier B) F	Register	C) Mi	ultiplexer	D) Magnitude Comparator			
9.	To decode the address for a ROM of size 256 x 8, we need an address decoder of size:								
16	A) 3 x 8 B) 8 x 256 Whick of the f	C) 1 D) 2 ollowing inputs	6 x 256 256 x 8 of a a JK flip	flop will t	oggle the pre	sent state?			
	A) J=0, K=0 B) J=1, K=0	C) 1	= 0, K = 1 = 1, K = 1		1777 T				